

IN THE CLAIMS:

Please amend the claims as follows:

1. (Currently Amended): A liquid crystal display, comprising:

a liquid crystal polarity inversion driver determining whether a polarity of a liquid crystal is inverted and inverting the polarity of the liquid crystal in accordance with the determined result;

a first data ~~polarity~~ inversion driver determining whether a first data number of transition ~~has occurred in a first set of odd data having a plurality of bits is more than half of the plurality of bits of the odd data~~, and inverting the ~~polarity of the first set of odd data~~ in accordance with the determined result; and

a second data ~~polarity~~ inversion driver determining whether a second data number of transition ~~has occurred in a second set of even data having a plurality of bits is more than half of the plurality of bits of the even data~~, and inverting the ~~polarity of the second set of even data~~ in accordance with the determined result;

~~wherein the first set of data is odd-numbered bits in an input data and the second set of data is even-numbered bits in the input data.~~

2. (Currently Amended): The liquid crystal display according to claim 1, wherein the first data

polarity inversion driver includes:

a first data transition part determining whether ~~[[the]]~~ a first data transition has occurred in the ~~first set of~~ odd data and outputting a first signal;

a first data ~~polarity~~ inversion signal summer counting the number of the first signal and outputting a first REV signal having a high level or a low level according to the counted number of the first signal ~~that a data polarity has changed according to the first data transition and determining whether an output level is high or low; and~~

a first data ~~polarity~~ inversion signal output part receiving ~~the first signal and the determined output level~~ the first REV signal from the ~~first data transition part and the first data~~ ~~polarity~~ inversion signal summer and outputting either the odd data or an inverted odd data according to the first REV signal ~~an inverting signal for inverting output data.~~

3. (Currently Amended): The liquid crystal display according to claim 1, wherein the second data ~~polarity~~ inversion driver includes:

a second data transition part determining whether ~~[[the]]~~ a second data transition has occurred in the ~~second set of~~ even data and outputting a second signal;

a second data ~~polarity~~ inversion signal summer counting the number of the second signal and outputting a second REV signal having a high level or a low level according to the counted number of the second signal ~~that a data polarity has changed according to the second data~~

~~transition and determining whether an output level is high or low; and~~

~~a second data polarity inversion signal output part receiving the second signal and the determined output level the second REV signal from the second data transition part and the second data polarity inversion signal summer and outputting the even data or an inverted even data according to the second REV signal an inverting signal for inverting output data.~~

4. (Currently Amended): The liquid crystal display according to claim 2, wherein the first data transition part includes first and second flip-flops and an exclusive logical sum gate comparing current odd data with previous odd data to determine whether the first data transition has occurred in accordance with the compared result.

5. (Currently Amended): The liquid crystal display according to claim 3, wherein the second data transition part includes first and second flip-flops and an exclusive logical sum gate comparing current even data with previous even data to determine whether the second data transition has occurred in accordance with the compared result.

6. (Currently Amended): The liquid crystal display according to claim 2, wherein the first data polarity inversion signal summer includes:

~~an adder adding~~ counting the number of data with a the first data transition from the first

data transition part; and

a majority detector determining whether the ~~added~~ counted number of the first data transition is higher than a first reference value.

7. (Currently Amended): The liquid crystal display according to claim 3, wherein the second data polarity inversion signal summer includes:

an adder ~~adding~~ counting the number ~~data with a~~ the second data transition from the second data transition part; and

a majority detector determining whether the ~~added~~ counted number of the second data transition is higher than a second reference value.

8. (Currently Amended): The liquid crystal display according to claim 2, wherein the first data ~~polarity~~ inversion signal output part includes a ~~multiplexer~~ multiplexer receiving a first ~~polarity~~ inversion signal from the first data ~~polarity~~ inversion signal summer to invert the output odd data.

9. (Currently Amended): The liquid crystal display according to claim 3, wherein the second data polarity inversion signal output part includes a ~~multiplexer~~ multiplexer receiving a second ~~polarity~~ inversion signal from the second data ~~polarity~~ inversion signal summer to invert the output even data.

10. (Canceled).

11. (Currently Amended): A method of driving a liquid crystal display having first and second data polarity inversion drivers, the method comprising:

dividing input data into a first set of odd data and a second set of even data;
~~inputting the first and second sets of data to the first and second data polarity inversion drivers,~~
comparing current odd data with previous odd data and current even data with previous even data
to determine a first data transition and a second data transition, respectively;

determining a number of first and second data transitions in the first odd and second sets
of even data, respectively; and

inverting a polarity of the first and second sets of the odd and even data in accordance
with the determined results, respectively.

~~wherein the first set of data is odd-numbered bits of the input data and the second set of
data is even-numbered bits of the input data.~~

12. (Currently Amended): The method according to claim 11, wherein inverting the polarity of
~~the first and second sets of~~ odd and even data includes:

~~comparing current data with previous data to determine whether the first and second data~~

~~transitions have occurred;~~

~~adding the number of the first and second sets of data having the first and second data transitions;~~

~~and~~

~~inverting the first and second sets of odd data and outputting the inverted odd data if the number of the added first data transition is more than $[[a]]$ half of a total number of the ~~input~~ odd data bit, and outputting the ~~input~~ odd data without an inversion if the number of the added first data transition is less than or equal to $[[a]]$ the half of the total number of the ~~input~~ odd data bit;~~
~~and~~

~~inverting the even data and outputting the inverted even data if the number of the second data transition is more than half of a total number of the even data bit, and outputting the even data without an inversion if the number of the second data transition is less than or equal to the half of the total number of the even data bit.~~

13. (Canceled).

14. (Currently Amended): The method according to claim 12, wherein the total number of the ~~input~~ odd data bit is 18.

15. (Canceled)

16. (Currently Amended): A 2-port data polarity inverter for driving a liquid crystal display, comprising:

an odd data ~~polarity~~ inversion driver to generate a first inversion signal to invert odd -
~~numbered input data bits when a first data transition is detected in the odd data~~ a number of a
first data transition is more than half of a total number of the odd data; and

an even data ~~polarity~~ inversion driver to generate a second inversion signal to invert even-
~~numbered input data bits when a second data transition is detected in the even data~~ a number of a
second data transition is more than half of a total number of the even data.

17. (Currently Amended): The 2-port data polarity inverter according to claim 16,

wherein the odd data ~~polarity~~ inversion driver includes

a first data transition part to determine whether the first data transition has
occurred in the odd data and outputting a first signal,

a first data ~~polarity~~ inversion signal summer ~~to count~~ counting the number of the
first signal and outputting a first REV signal having a high level or a low level according
to the counted number of the first signal ~~that a data polarity has changed according to the~~
~~first data transition and determining whether an output level is high or low, and~~

a first data ~~polarity~~ inversion signal output part receiving the first REV signal
from the first data inversion signal summer and outputting the odd data or an inverted odd

~~data according to the first REV signal to receive the first signal and the determined output level from the first data transition part and the first data polarity inversion signal summer and outputting the first inversion signal; and~~

wherein the second data polarity inversion driver includes

a second data transition part to determine whether the second data transition has occurred in the even data and outputting a second signal,

a second data polarity inversion signal summer ~~to count~~ counting the number of the second signal and outputting a second REV signal having a high level or a low level according to the counted number of the second signal that a data polarity has changed according to the second data transition and determining whether an output level is high or low, and

a second data polarity inversion signal output part receiving the second REV signal from the second data inversion signal summer and outputting the even data or an inverted even data according to the second REV signal to receive the second signal and the determined output level from the second data transition part and the second data polarity inversion signal summer and outputting the second inversion signal.

18. (Currently Amended): The 2-port data polarity inverter according to claim 17,

wherein the first data transition part includes first and second flip-flops and a first

exclusive logical sum gate to compare current odd data with previous odd data to determine whether the first data transition has occurred in accordance with the compared result, and

wherein the second data transition part includes ~~second and third~~ and fourth flip-flops and a second exclusive logical sum gate to compare current even data with previous even data to determine whether the second data transition has occurred in accordance with the compared result.

19. (Currently Amended): The 2-port data polarity inverter according to claim 17,

wherein the first data ~~polarity~~ inversion signal summer includes a first adder ~~to add~~ counting the number of data ~~with a the~~ the first data transition from the first data transition part, and a first majority detector to determine whether the ~~added~~ counted number of the first data transition is higher than a first reference value; and

wherein the second data ~~polarity~~ inversion signal summer includes a second adder ~~to add~~ counting the number of data ~~with a the~~ the second data transition from the second data transition part; and a second majority detector to determine whether the ~~added~~ counted number of the second data transition is higher than a second reference value.

20. (Currently Amended): The 2-port data polarity inverter according to claim 17,

wherein the first data ~~polarity~~ inversion signal output part includes a first multiplexer

~~receiving the multiplexer receive a first polarity inversion~~ REV signal from the first data ~~polarity~~
inversion signal summer to invert the ~~output~~ odd data, and

wherein the second data ~~polarity~~ inversion signal output part includes a second
~~multiplexer receiving the multiplexer to receive a second polarity inversion~~ REV signal from the
second data ~~polarity~~ inversion signal summer to invert the ~~output~~ even data.